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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/648,154
				Filing Date	08/26/2003
				First Named Inventor	Pechanek
				Art Unit	
				Examiner Name	
				Attorney Docket Number	138.0001
Sheet	1	of	1		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BLAUJW ET AL., Computer Architecture: Concepts and Evolution, 1997, Page(s) 71-75, 589-590, 648-664, Publisher: Addison-Wesley, Published In: Reading, Mass.	
		JOHNSON, An Introduction to Vector Processing, Computer Design, February 1978, Page(s) 89-97	
		PECHANKE ET AL., The ManArray Embedded Processor Architecture, Proceedings of the 26th EUROMICRO Conference, 2000, Page(s) 348-355, Publisher: IEEE Computer Society	
		PELEG ET AL., Intel MMX for Multimedia PCs, Communications of the ACM, January 1997, Page(s) 25-38, Volume 40, Number 1	
Examiner Signature 	Date Considered 3/14/06 		

*EXAMINER: Initial if reference considered whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.
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